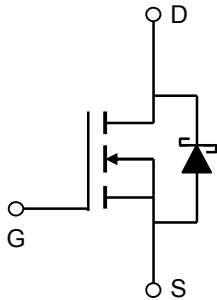
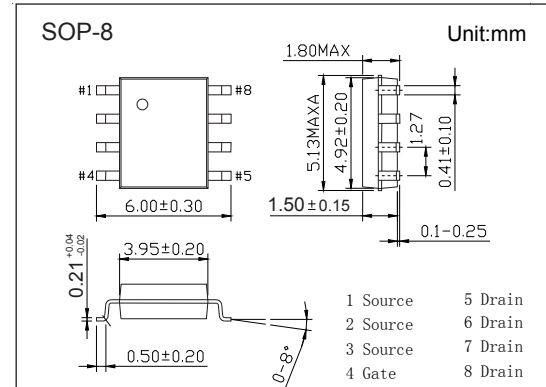


N-Channel MOSFET

AO4710 (KO4710)

■ Features

- $V_{DS} (V) = 30V$
- $I_D = 12.7 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 11.8m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 14.2m\Omega (V_{GS} = 4.5V)$
- SRFET™ Soft Recovery MOSFET: Integrated Schottky Diode



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	$T_A=25^\circ C$	12.7
		$T_A=70^\circ C$	10
Pulsed Drain Current	I_{DM}	60	A
Avalanche Current	I_{AR}	22	A
Repetitive Avalanche Energy	$L=0.3mH$	E_{AR}	73
Power Dissipation	P_D	$T_A=25^\circ C$	3.1
		$T_A=70^\circ C$	2
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	40
		Steady-State	75
Thermal Resistance.Junction- to-Lead	R_{thJL}	24	$^\circ C/W$
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150	$^\circ C$

N-Channel MOSFET

AO4710 (KO4710)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V _{DSS}	I _D =1mA, V _{GS} =0V	30			V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			0.1	mA	
		V _{DS} =30V, V _{GS} =0V, T _J =125°C			20		
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250 μA	1.5		2.3	V	
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =12.7A			11.8	mΩ	
		V _{GS} =10V, I _D =12.7A, T _J =125°C			19		
		V _{GS} =4.5V, I _D =11A			14.2		
On State Drain Current	I _{D(ON)}	V _{GS} =10V, V _{DS} =5V	60			A	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =12.7A		78		S	
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, f=1MHz		1980	2376	pF	
Output Capacitance	C _{oss}			317			
Reverse Transfer Capacitance	C _{rss}			111			
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		1.3	2	Ω	
Total Gate Charge (10V)	Q _g	V _{GS} =10V, V _{DS} =15V, I _D =12.7A		33	43	nC	
Total Gate Charge (4.5V)				15	20		
Gate Source Charge			Q _{gs}		5.3		
Gate Drain Charge			Q _{gd}		6		
Turn-On DelayTime	t _{d(on)}	V _{GS} =10V, V _{DS} =15V, R _L =1.2Ω, R _{GEN} =3Ω		5.5		ns	
Turn-On Rise Time	t _r			5.5			
Turn-Off DelayTime	t _{d(off)}			27			
Turn-Off Fall Time	t _f			4.3			
Body Diode Reverse Recovery Time	t _{rr}	I _F = 12.7A, di/dt= 300A/us		11.2	13	nC	
Body Diode Reverse Recovery Charge	Q _{rr}			7			
Maximum Body-Diode Continuous Current	I _S				5	A	
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V			0.5	V	

Note.The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

■ Marking

Marking	4710 KC****
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N-Channel MOSFET AO4710 (KO4710)

■ Typical Characteristics

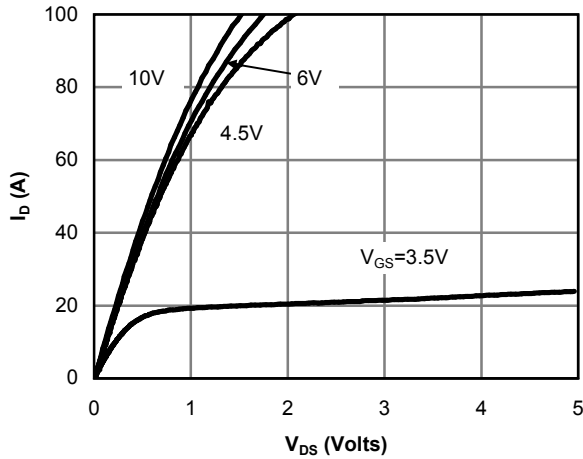


Figure 1: On-Region Characteristics

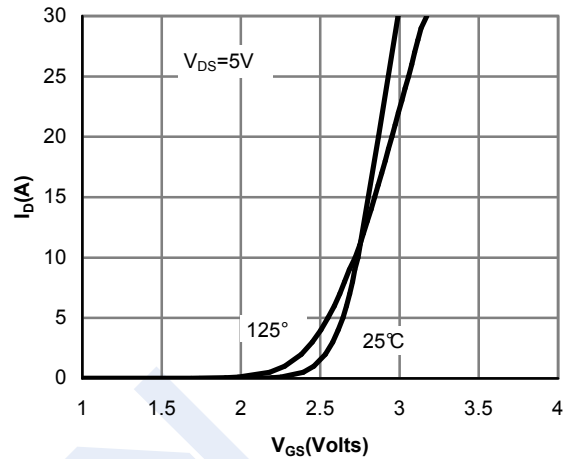


Figure 2: Transfer Characteristics

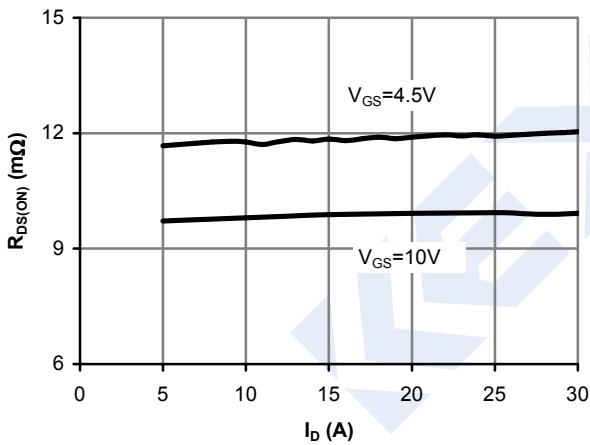


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

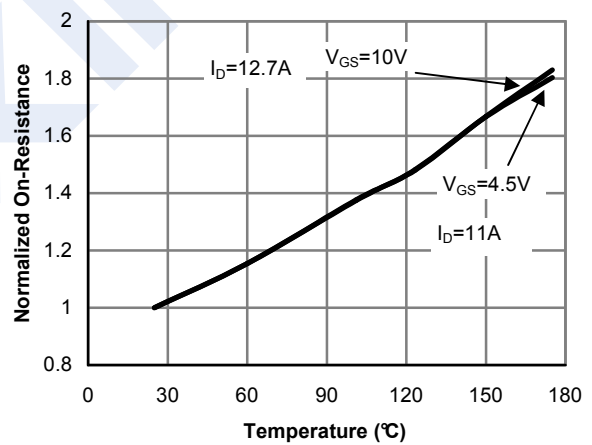


Figure 4: On-Resistance vs. Junction Temperature

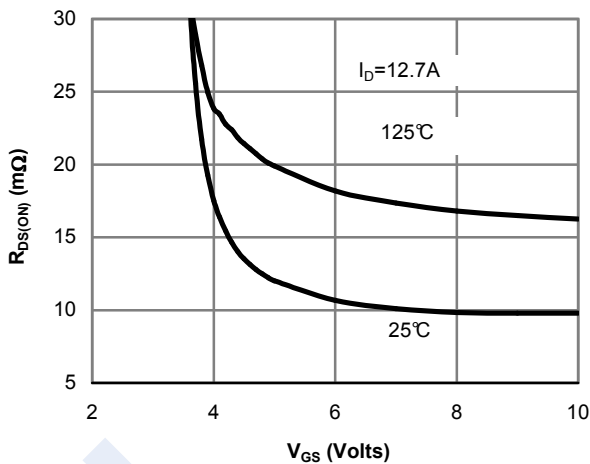


Figure 5: On-Resistance vs. Gate-Source Voltage

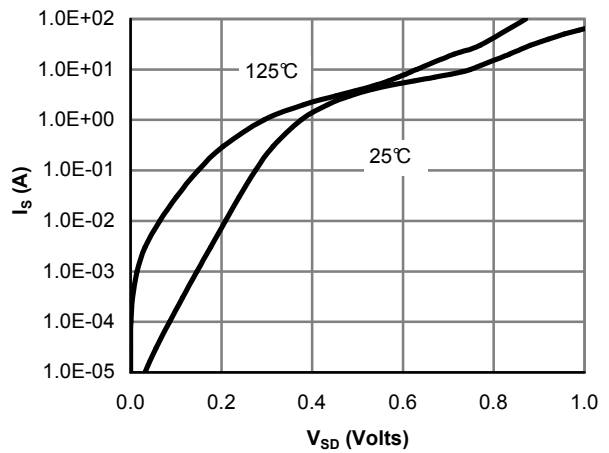


Figure 6: Body-Diode Characteristics

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■ Typical Characteristics

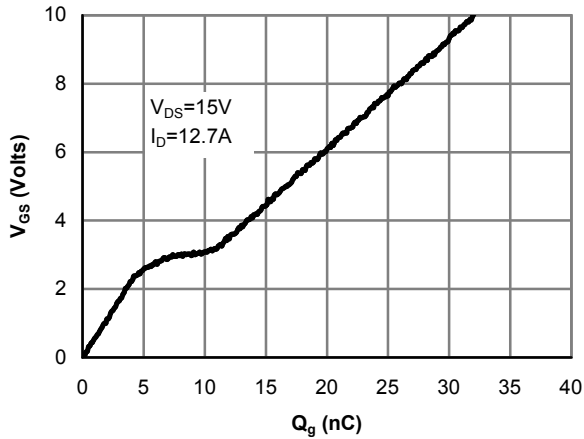


Figure 7: Gate-Charge Characteristics

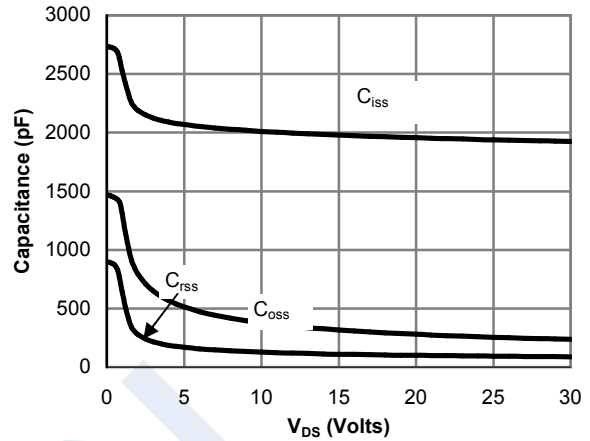


Figure 8: Capacitance Characteristics

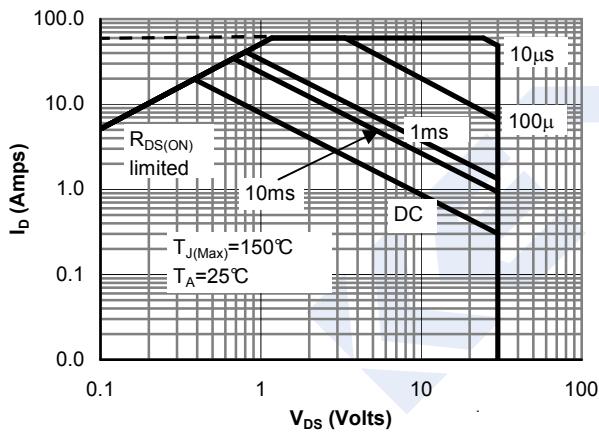


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

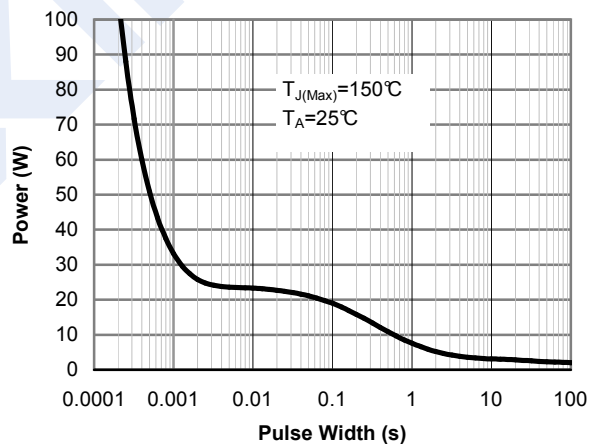


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

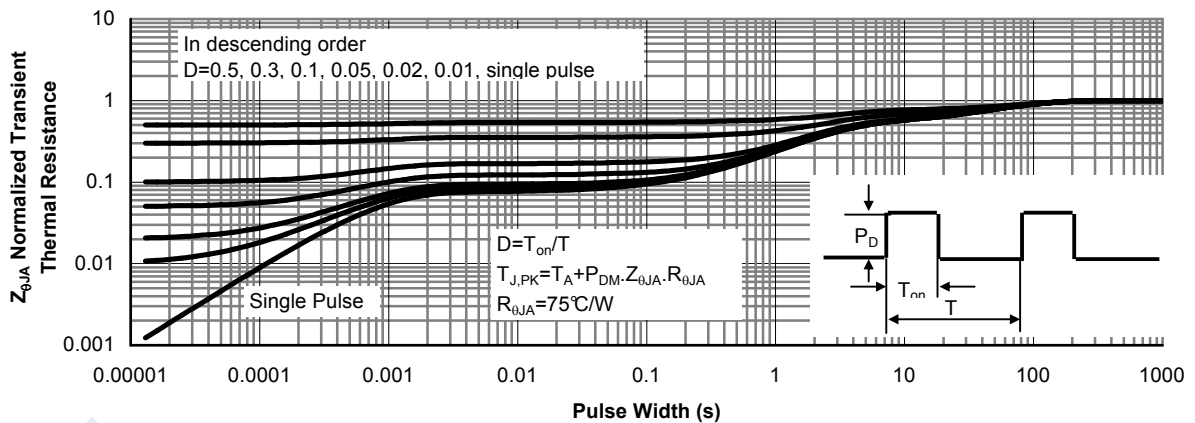


Figure 11: Normalized Maximum Transient Thermal Impedance (Note E)